

Docket No.: Y2238.0002/P002
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Itaru Nishioka, et al.

Application No.: Not Yet Assigned

Group Art Unit: N/A

Filed: Herewith

Examiner: Not Yet Assigned

For: NETWORK NODE APPARATUS,
NETWORK SYSTEM USING THE SAME
AND FAULT LOCATION DETECTING
METHOD

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, DC 20231

Dear Sir:

Preliminary to examination, please amend the above-referenced application as follows:

IN THE SPECIFICATION:

Rewrite the paragraph at page 12, line 6, as follows:

FIGS. 3A through 3D are block diagrams showing an internal configuration of each of determination devices 11a to 11h as shown in FIG. 2;

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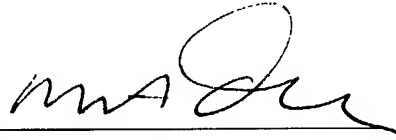
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REMARKS

The application has been editorially revised for consistency between the specification and the drawings. Favorable action on the application is solicited.

Dated: March 4, 2002

Respectfully submitted,

By 

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MARKED-UP VERSION SHOWING CHANGES MADE

[FIG. 3 is a | FIGS. 3A through 3D are block [diagram] diagrams showing an internal configuration of each of determination devices 11a to 11h as shown in FIG. 2;